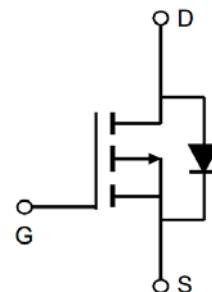
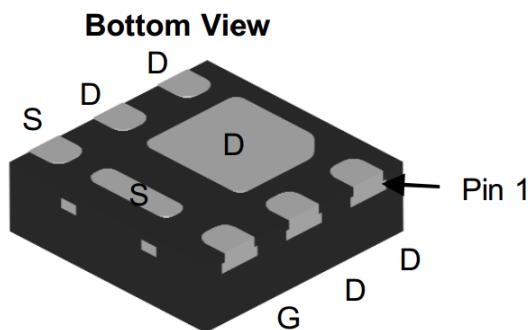


## P-Channel -20V (D-S) MOSFET

| ● FEATURES  | ● GENERAL DESCRIPTION   |
|---|---|
| RDS(ON) 21mΩ@VGS=-4.5V<br>RDS(ON) 26mΩ@VGS=-2.5V<br><br>high density cell design for extremely low RDS(ON)<br>Exceptional on-resistance and maximum DC current capability | The FS1256 combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$ . This device is ideal for load switch and battery protection applications. |

### ● PIN CONFIGURATION



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ Unless Otherwise Noted)

| Parameter  | Symbol         | Limit      | Unit             |
|--|----------------|------------|------------------|
| Drain-Source Voltage                             | $V_{DS}$       | -20        | V                |
| Gate-Source Voltage                              | $V_{GS}$       | $\pm 10$   | V                |
| Drain Current-Continuous                         | $I_D$          | -3.5       | A                |
| Drain Current -Pulsed (Note )                    | $I_{DM}$       | -20        | A                |
| Maximum Power Dissipation                        | $P_D$          | 18         | W                |
| Operating Junction and Storage Temperature Range | $T_J, T_{STG}$ | -55 To 150 | $^\circ\text{C}$ |

●

### Thermal Characteristics

| Parameter                            | Symbol    | Limit | Units                     |
|--------------------------------------|-----------|-------|---------------------------|
| Thermal Resistance, Junction-to-Case | $R_{qJC}$ | 6.9   | $^\circ\text{C}/\text{W}$ |

NOTE:

A: Surface mounted on FR4 Board using 1 in sq pad size, 1oz Cu.

B: Surface mounted on FR4 board using the minimum recommended pad size, 1oz Cu.

C: Repetitive rating, pulse width limited by junction temperature,  $t_p=10\mu\text{s}$ , Duty Cycle=1%

D: Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .

# FS1256

- Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise noted)

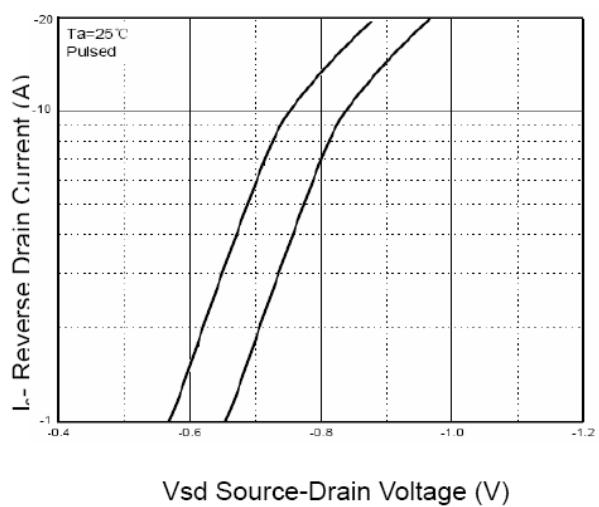
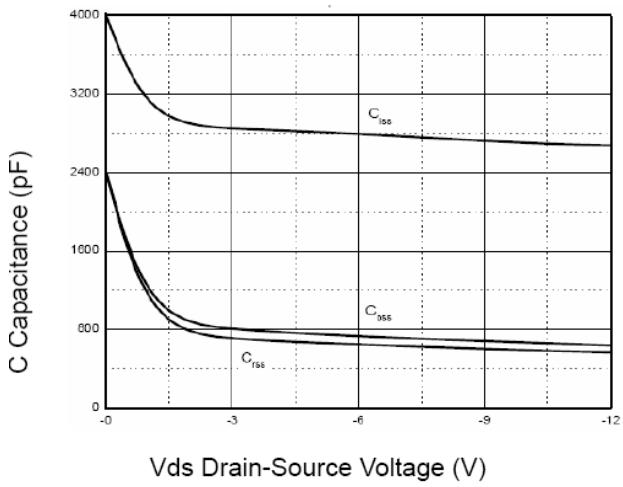
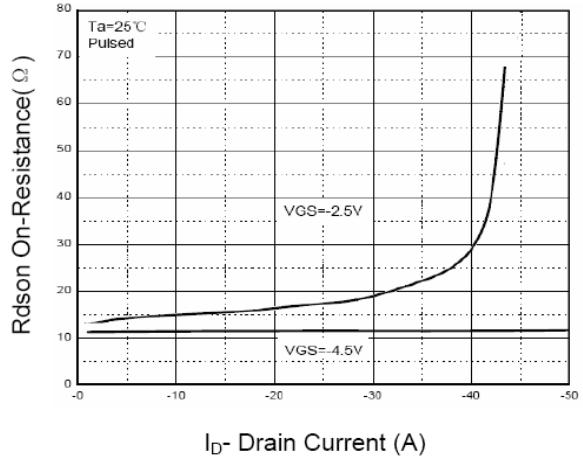
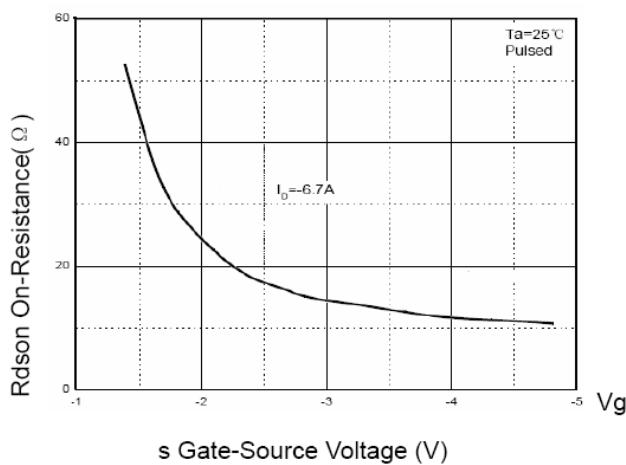
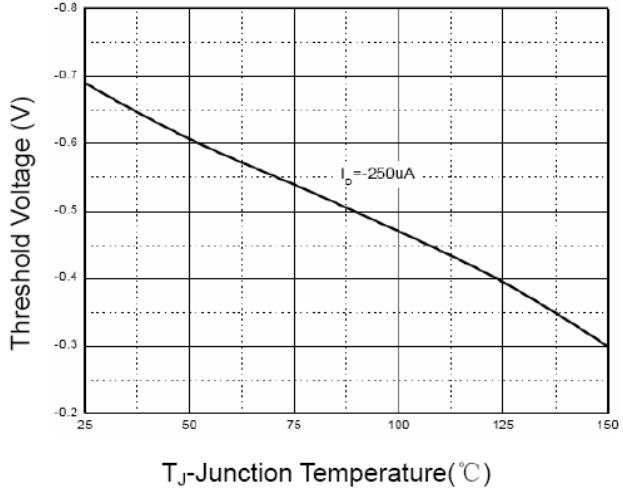
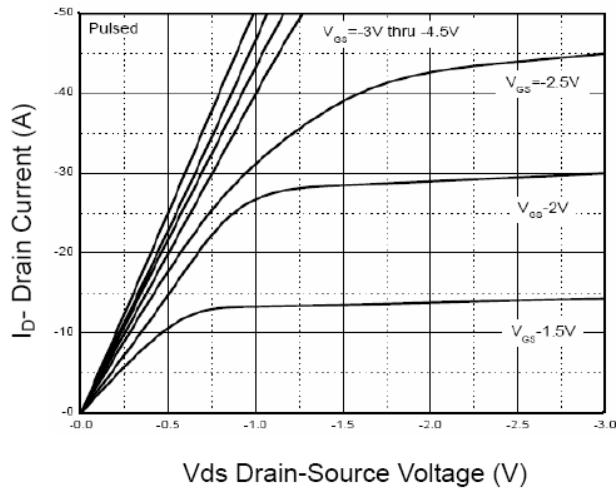
| Parameter                                 | Symbol                      | Condition   | Min  | Typ   | Max       | Unit             |
|---|-----------------------------|---|------|-------|-----------|------------------|
| <b>Off Characteristics</b>                |                             |   |      |       |           |                  |
| Drain-Source Breakdown Voltage            | $V_{(\text{BR})\text{DSS}}$ | $V_{GS}=0\text{V}, I_D=-250\mu\text{A}$   | -20  | -     | -         | V                |
| Zero Gate Voltage Drain Current           | $I_{DSS}$                   | $V_{DS}=-8\text{V}, V_{GS}=0\text{V}$   | -    | -     | -1        | $\mu\text{A}$    |
| Gate-Body Leakage Current                 | $I_{GSS}$                   | $V_{GS}=\pm 8\text{V}, V_{DS}=0\text{V}$  | -    | -     | $\pm 100$ | nA               |
| <b>On Characteristics</b> (Note 3)        |                             |   |      |       |           |                  |
| Gate Threshold Voltage                    | $V_{GS(\text{th})}$         | $V_{DS}=V_{GS}, I_D=-250\mu\text{A}$  | -0.4 | -0.65 | -1.0      | V                |
| Drain-Source On-State Resistance          | $R_{DS(\text{ON})}$         | $V_{GS}=-4.5\text{V}, I_D=-6.6\text{A}$   | -    | 21    | 29        | $\text{m}\Omega$ |
|   |                             | $V_{GS}=-2.5\text{V}, I_D=-3.5\text{A}$   | -    | 26    | 39        | $\text{m}\Omega$ |
|   |                             | $V_{GS}=-1.8\text{V}, I_D=-2.0\text{A}$   | -    | 35    | 47        | $\text{m}\Omega$ |
| Forward Transconductance                  | $g_{FS}$                    | $V_{DS}=-5\text{V}, I_D=-6.7\text{A}$   | 20   | -     | -         | S                |
| <b>Dynamic Characteristics</b> (Note 4)   |                             |   |      |       |           |                  |
| Input Capacitance                         | $C_{iss}$                   | $V_{DS}=-10\text{V}, V_{GS}=0\text{V}, F=1.0\text{MHz}$                         | -    | 2100  | -         | PF               |
| Output Capacitance                        | $C_{oss}$                   |   | -    | 540   | -         | PF               |
| Reverse Transfer Capacitance              | $C_{rss}$                   |   | -    | 470   | -         | PF               |
| <b>Switching Characteristics</b> (Note 4) |                             |   |      |       |           |                  |
| Turn-on Delay Time                        | $t_{d(on)}$                 | $V_{DD}=-10\text{V}, I_D=-1\text{A}$<br>$V_{GS}=-4.5\text{V}, R_{GEN}=10\Omega$ | -    | 11    | -         | nS               |
| Turn-on Rise Time                         | $t_r$                       |   | -    | 35    | -         | nS               |
| Turn-Off Delay Time                       | $t_{d(off)}$                |   | -    | 30    | -         | nS               |
| Turn-Off Fall Time                        | $t_f$                       |   | -    | 10    | -         | nS               |
| Total Gate Charge                         | $Q_g$                       | $V_{DS}=-6\text{V}, I_D=-10\text{A}$ ,<br>$V_{GS}=-4.5\text{V}$                 | -    | 35    | 48        | nC               |
| Gate-Source Charge                        | $Q_{gs}$                    |   | -    | 5     | -         | nC               |
| Gate-Drain Charge                         | $Q_{gd}$                    |   | -    | 10    | -         | nC               |
| <b>Drain-Source Diode Characteristics</b> |                             |   |      |       |           |                  |
| Diode Forward Voltage (Note 3)            | $V_{SD}$                    | $V_{GS}=0\text{V}, I_s=-1\text{A}$  | -    | -     | -1.2      | V                |
| Diode Forward Current (Note 2)            | $I_s$                       |   | -    | -     | -1.6      | A                |

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

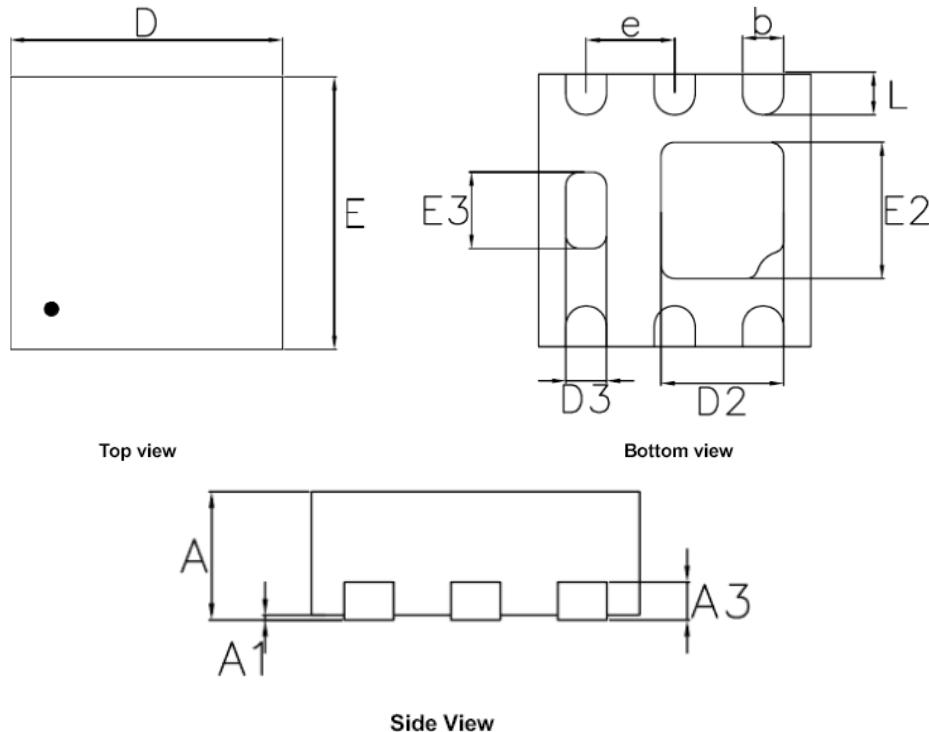
# FS1256

- Typical Performance Characteristics ( $T = 25^\circ\text{C}$ )



# FS1256

- DFN2X2-6L Package Information



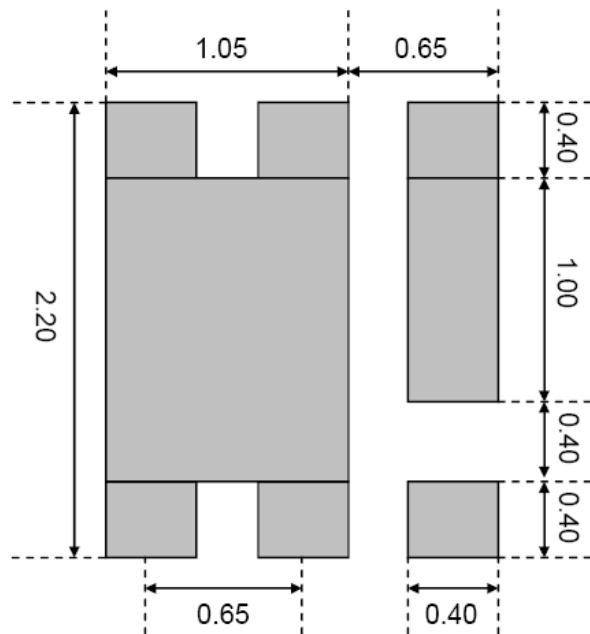
| Symbol | Dimensions in millimeter |      |      |
|--------|--------------------------|------|------|
|        | Min.                     | Typ. | Max. |
| A      | 0.70                     | 0.75 | 0.80 |
| A1     | 0.00                     | -    | 0.05 |
| A3     | 0.20 Ref.                |      |      |
| D      | 1.95                     | 2.00 | 2.05 |
| E      | 1.95                     | 2.00 | 2.05 |
| D2     | 0.85                     | 0.90 | 0.95 |
| E2     | 0.95                     | 1.00 | 1.05 |
| D3     | 0.25                     | 0.30 | 0.35 |
| E3     | 0.51                     | 0.56 | 0.61 |
| b      | 0.25                     | 0.30 | 0.35 |
| L      | 0.25                     | 0.30 | 0.30 |
| e      | 0.65 BSC.                |      |      |

## Notes

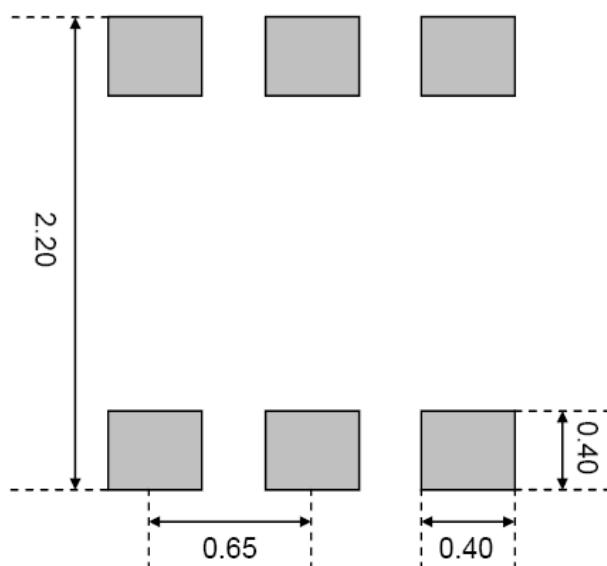
1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

# FS1256

## Recommend PCB Layout (Unit: mm)



Option 1: High power applications



Option 2: Normal applications