

## Dual 300mA High Speed LDO

### ● Features

- High Ripple Rejection : 70dB (1 kHz)
- Low Power Consumption : 40 $\mu$ A (TYP.)
- Maximum Output Current : 300mA
- Standby Current : less than 0.1 $\mu$ A
- Internal protector : current limiter and short protector

### ● Applications

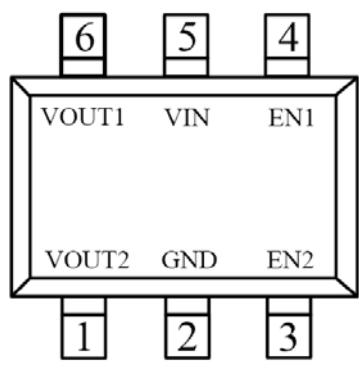
- Blue Tooth Headsets
- Portable Audio Players
- Mobile Phones
- Wireless and DSL Modems
- Digital Cameras
- Portable Instruments

### ● General Description

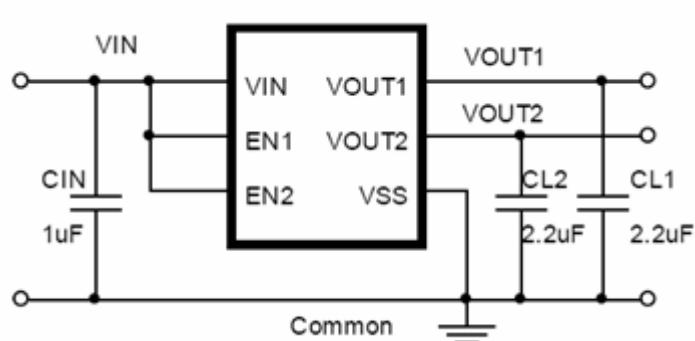
The FS3403 series are highly accurate, Dual, low noise, CMOS LDO voltage regulators. Performance features of the series includes low output noise, high ripple rejection ratio, low dropout and very fast turn-on times.

The FS3403 includes a reference voltage source, error amplifiers, driver transistors, current limiters and phase compensators internally. The FS3403's current limiter's foldback circuit also operates as a short protect for the output current limiter. The output voltage for each regulator is set independently by laser trimming. Voltages are selectable in 50mV steps within a range of 1.3V to 5.0V. The EN function allows the output of each regulator to be turned off independently, resulting in greatly reduced power consumption.

### ● Pin Configurations



### ● Typical Application Circuit



# FS3403

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## ● Absolute Maximum Ratings

Parameter	Symbol	Maximum Rating		Unit	
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> -0.3~V <sub>SS</sub> +10		V	
	V <sub>EN</sub>	V <sub>SS</sub> -0.3~V <sub>IN</sub> +0.3			
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.3~V <sub>IN</sub> +0.3			
Output Current	I <sub>OUT1</sub> +I <sub>OUT2</sub>	700		mA	
Power Dissipation	P <sub>D</sub>	SOT-23-6L	250	mW	
Operating Ambient Temperature	T <sub>OPR</sub>	-40~+85		°C	
Storage Temperature	T <sub>STG</sub>	-40~+125			

Note1: Production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.

## ● Electrical Characteristics

Parameter	Symbol	CONDITION	MIN	TYP	MAX	UNIT
Output Voltage	V <sub>OUT(E)</sub>	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V, I <sub>OUT</sub> =10 mA	V <sub>OUT(S)</sub> ×0.98	V <sub>OUT(S)</sub>	V <sub>OUT(S)</sub> ×1.02	V
Output Current	I <sub>OUT</sub>	V <sub>IN</sub> ≥V <sub>OUT(S)</sub> +1.0 V	300	—	—	mA
Dropout Voltage	V <sub>drop</sub>	I <sub>OUT</sub> =50 mA	—	0.06	0.10	V
		I <sub>OUT</sub> =100 mA	—	0.15	0.20	
Line Regulations	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	V <sub>OUT(S)</sub> +0.5 V ≤ V <sub>IN</sub> ≤8 V I <sub>OUT</sub> =10 mA	—	0.01	0.2	%/V
Load Regulation	$\Delta V_{OUT2}$	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V 1.0 mA ≤ I <sub>OUT</sub> ≤100 mA	—	15	50	mV
Output Voltage Temperature Characteristics	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V, I <sub>OUT</sub> =10 mA -40°C ≤ 85°C T <sub>a</sub>	—	±100	—	ppm/°C
Supply Current	I <sub>SS1</sub>	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V	—	40	80	μA
Standby Current	I <sub>STB</sub>	V <sub>IN</sub> =V <sub>EN</sub> =V <sub>OUT(T)</sub> +1V , V <sub>EN</sub> =V <sub>SS</sub>	—	0.01	1	μA
Input Voltage	V <sub>IN</sub>	—	2.0	—	10	V
Ripple-Rejection	PSRR	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V, f=1 kHz V <sub>rip</sub> =0.5 Vrms, I <sub>OUT</sub> =30 mA	—	70	—	dB
Short-circuit Current	I <sub>short</sub>	V <sub>IN</sub> =V <sub>OUT(S)</sub> +1.0 V, V <sub>IN</sub> =V <sub>EN</sub>	—	30	—	mA
Current limiter	I <sub>lim</sub>	V <sub>IN</sub> =V <sub>EN</sub> =V <sub>OUT(T)</sub> +1V	—	450	—	mA
CE "High" Voltage	V <sub>CEH</sub>	—	1.3	—	V <sub>IN</sub>	V
CE "Low" Voltage	V <sub>CEL</sub>	—	—	0.25	—	V
CE "High" Current	I <sub>CEH</sub>	V <sub>IN</sub> =V <sub>EN</sub> =V <sub>OUT(T)</sub> +1V	-0.1	—	0.1	uA
CE "Low" Current	I <sub>CEH</sub>	V <sub>IN</sub> =V <sub>EN</sub> =V <sub>OUT(T)</sub> +1V , V <sub>EN</sub> =V <sub>SS</sub>	-0.1	—	0.1	uA

# FS3403

- Ordering Information

**FS3403** (1) (2) (3) (4) (5) (6) (7) (8)

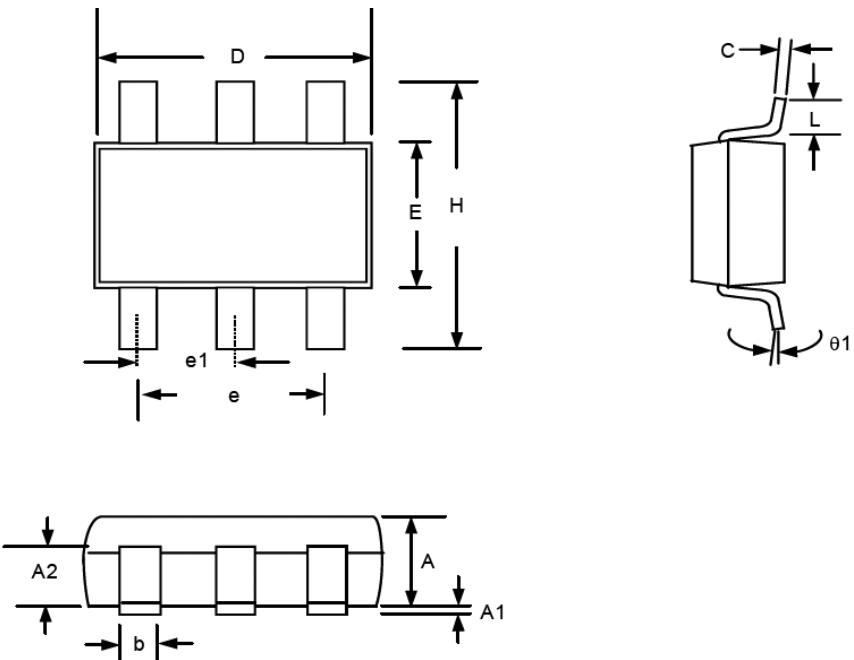
Designator	Description	Symbol	Description
①	Regulator1 EN type	E	Active 'High' (pull-down resistor built in)
		F	Active 'High' (no pull-down resistor built in)
		G	Active 'Low' (pull-up resistor built in)
		H	Active 'Low' (no pull-up resistor built in)
②	Regulator2 EN type	E	Active 'High' (pull-down resistor built in)
		F	Active 'High' (no pull-down resistor built in)
		G	Active 'Low' (pull-up resistor built in)
		H	Active 'Low' (no pull-up resistor built in)
③④	Regulator1 Output Voltage	13~50	eg. 30 represents 3.0V
⑤⑥	Regulator2 Output Voltage	13~50	eg. 30 represents 3.0V
⑦	Packaging Types	M	SOT-23-6L(Vout1>Vout2)
		N	SOT-23-6L(Vout1<Vout2)
⑧	Device Orientation	R	Embossed tape: Standard feed
		L	Embossed tape: Reverse feed

- Pin Description

Pin Number	Pin Name	Function
SOT-23-6L		
4	EN1	ON/OFF Control1
5	VIN	Power Input
3	EN2	ON/OFF Control2
1	VOUT2	Output2
2	GND	Ground
6	VOUT1	Output 1

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- Package Information



Symbol	Dimension mm			Dimension in inch		
	Min	Nom	Max	Min	Nom	Max
A	1.00	1.10	1.30	0.039	0.043	0.051
A1	0.00		0.10	0.000		0.004
A2	0.70	0.80	0.90	0.028	0.031	0.035
b	0.35	0.40	0.50	0.014	0.016	0.020
C	0.10	0.15	0.25	0.004	0.006	0.010
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.40	1.60	1.80	0.055	0.063	0.071
e		1.90(TYP)			0.075(TYP)	
H	2.60	2.80	3.00	0.102	0.110	0.118
L	0.37			0.015		
θ1	1°	5°	9°	1°	5°	9°