

## High Driver LDO Regulator

### ● Features

- Low Power Consumption 3.0  $\mu$  A ( TYP. )
- Low voltage drop
- Low temperature coefficient
- High input voltage (Up to 30V )
- High input current:100mA ( Pd:250mW )
- Low power consumption
- Ceramic compatible
- TO92 & SOT89 package

### ● General Description

The FS10XX series is a set of three-terminal high current low voltage regulator implemented in CMOS technology. They

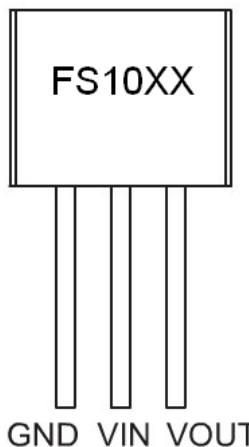
can deliver 150mA output current and allow an input voltage as high as 30V. They are available with several fixed output voltages ranging 3.0V 3.3V 3.6V 5.0V. CMOS technology ensures low voltage drop and low quiescent current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain variable voltages and currents.

### ● Applications

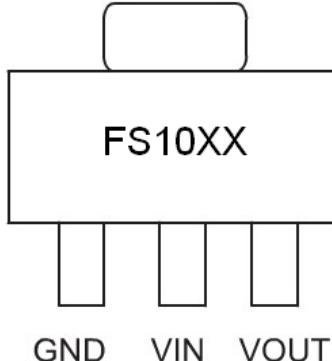
- Battery powered equipment
- Audio/Video equipment
- Communication equipment

### ● Package Information

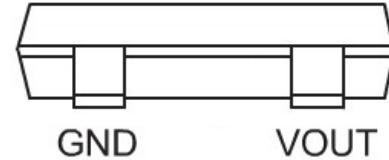
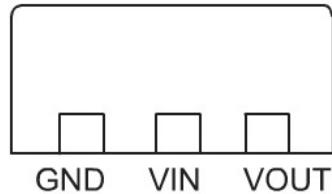
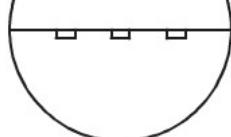
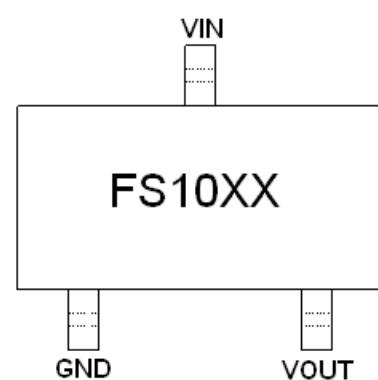
**TO92**



**SOT89**

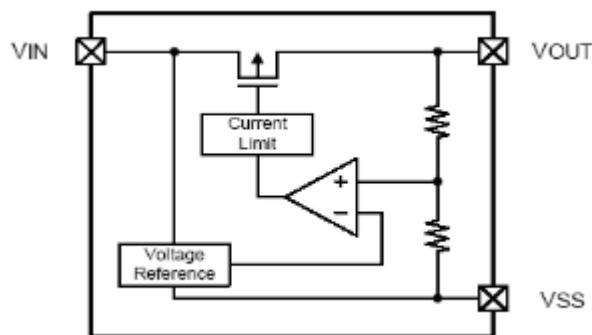


**SOT23**

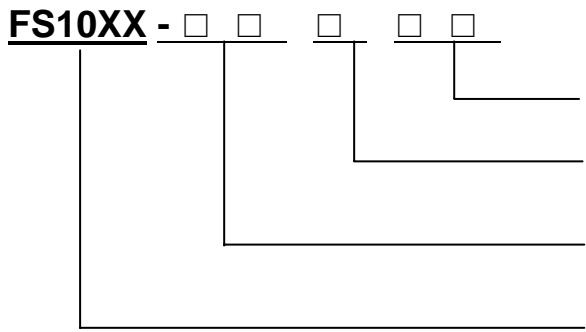


# FS10xx

## Functional Block Diagram



## ● Ordering information



Package type  
TA=TO92; SM=SOT89

Output Voltage Accuracy

2:  $\pm 2.0\%$

Output Voltage

... 30=3.0V 33=3.3V 50=5.0V ...

## ● Absolute Maximum Ratings

Parameter	Symbol	Ratings	Units
Input Voltage	$V_{IN}$	30	V
Output Current	$I_{OUT}$	150	mA
Output Voltage	$V_{OUT}$	$V_{SS}-0.3$ to $V_{IN}+0.3$	V
Operating Ambient Temperature	$T_{OPR}$	-25 to + 85	°C
Storage Temperature	$T_{STG}$	-40 to + 125	°C
Continuous Total Power Dissipation	$P_D$	700	mW
		500	
Lead Temperature (Soldering) 10 seconds	$T_{SOLDER}$	260	°C

**Note:** Operating near the absolute maximum ratings may affect the device's reliability or make the device damage

# FS10xx

## ● Electrical Characteristics

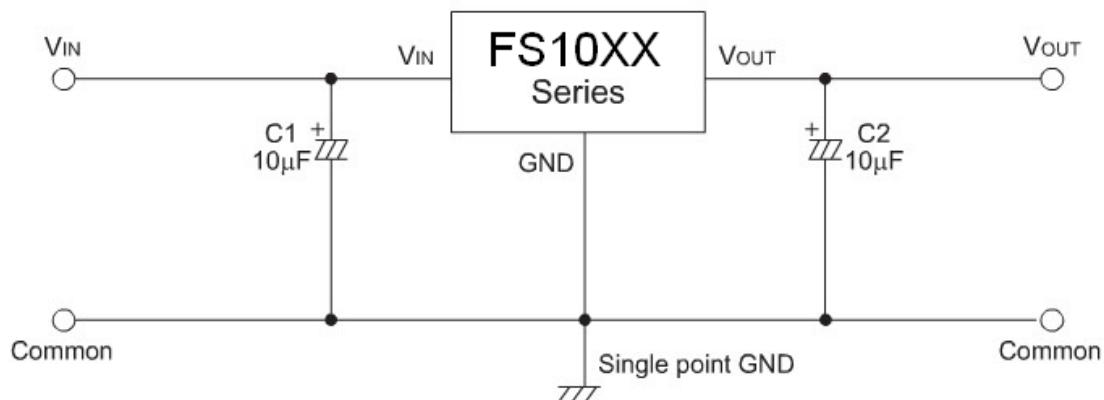
$V_{in} = V_{out}(s) + 2V$ ,  $C_{in} = C_{out} = 10\mu F$  electronic,  $T_a = 25^\circ C$ , Unless otherwise specified (Note1)

Parameter	Symbol	conditions	Min	Typ	Max	Unit
Output Voltage	$V_{out}(E)$ (Note2)	$I_{out} = 40mA$ $V_{in} = V_{out}(\text{Test}) + 2V$	$V_{out}(s)$ $\times 0.98$		$V_{out}(s)$ $\times 1.02$	V
Input Voltage	$V_{in}$				20	V
Maximum Output current	$I_{out \text{ max}}$		150			mA
Load Regulation	$\Delta V_{out}$	$V_{in} = V_{out} + 2V$ $1mA \leq I_{out} \leq 150mA$		30		mV
Dropout Voltage ( Note3 )	$V_{dif}$	$I_{out} = 1mA$		100		mV
		$I_{out} = 10mA$		160		
		$I_{out} = 40mA$		650		
Supply Current	$I_{ss}$	$V_{in} = V_{out}(S) + 2V$		3		uA
Line Regulation	$\frac{\Delta V_{out}}{\Delta V_{out} \times \Delta V_{in}}$	$I_{out} = 40mA$ $V_{out} + 2V \leq V_{in} \leq 20V$		0.3		%/V

### Note:

1.  $V_{out} ( S )$  = Specified output Voltage
2.  $V_{out} ( E )$  = Effective output Voltage ( i.e. the output voltage when " $V_{out} ( \text{Test} ) + 2.0V$ " is provided at the  $V_{in}$  pin while maintaining a certain  $I_{out}$  value )
3.  $V_{drop} = \{ V_{in1} (\text{note5}) - V_{out1} (\text{note4}) \}$
4.  $V_{out1}$  = A voltage equal to 98% of the output voltage whenever an amply stabilized  $I_{out}$  ( $V_{out} ( T ) + 2.0V$ ) is input
5.  $V_{in1}$  = The input voltage when  $V_{out} = V_{out1}$

## ● Typical Application Circuit



# FS10xx

- **Typical Performance Characteristics** (For FS1033 2SM)

